

ABSTRACT OF THE DISCLOSURE

To provide a turbo decoder reducing power consumption by reducing an operational processing amount while ensuring sufficient error correction capability, there is provided a constitution including an input signal memory for storing turbo-coded received data, a decoder for executing SOVA decoding, a decoded result memory for storing a decoded result, a control block for executing repeated decoding, a memory stored with an interleave pattern, and judging means for judging reliability of a decoded result and the reliability of the decoded result is judged based on a soft decision value outputted from the SOVA decoder and when desired reliability is satisfied by the judgment result, the repeated decoding is finished.